

and main rotator means for connecting each input port cyclically to a timeslot of the central switching unit.

<sup>2</sup>  
~~10.~~ The switch as claimed in Claim <sup>1</sup>~~9~~; and further comprising a second rotator means to connect each timeslot of the central switching unit cyclically to each output port.

D3 <sup>3</sup>  
~~11.~~ The switch as claimed in Claim <sup>1</sup>~~9~~, wherein the main rotator means comprises a single stage of rotators.

<sup>4</sup>  
~~12.~~ The switch as claimed in Claim <sup>1</sup>~~9~~, wherein the main rotator means comprises multi-stage rotators.

<sup>5</sup>  
~~13.~~ The switch as claimed in Claim <sup>2</sup>~~10~~, wherein each rotator means comprises a single stage of rotators.

<sup>6</sup>  
~~14.~~ The switch as claimed in Claim <sup>2</sup>~~10~~, wherein each rotator means comprises multi-stage rotators. --

#### REMARKS

The Examiner has requested clarification of the term "CLOS" at page 4, line 6 of the specification. CLOS is a standard term used in switch traffic modeling, named after a gentleman named CLOS who demonstrated that for a switch to be non-blocking with N inputs, the switch has to have at least  $2N-1$  paths across its center. Thus, the meaning of the phrase cited is that the capacity would be less than that of a switch conforming to the CLOS standard.

Continuation information has been provided on page 1 of the application.